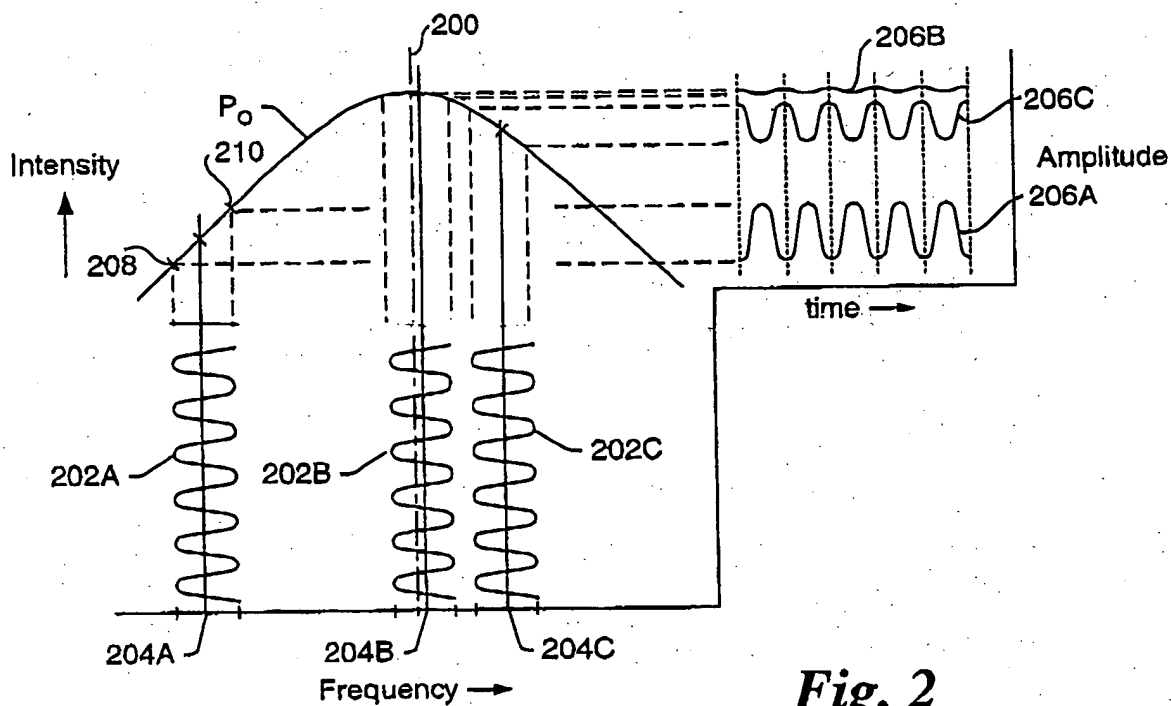
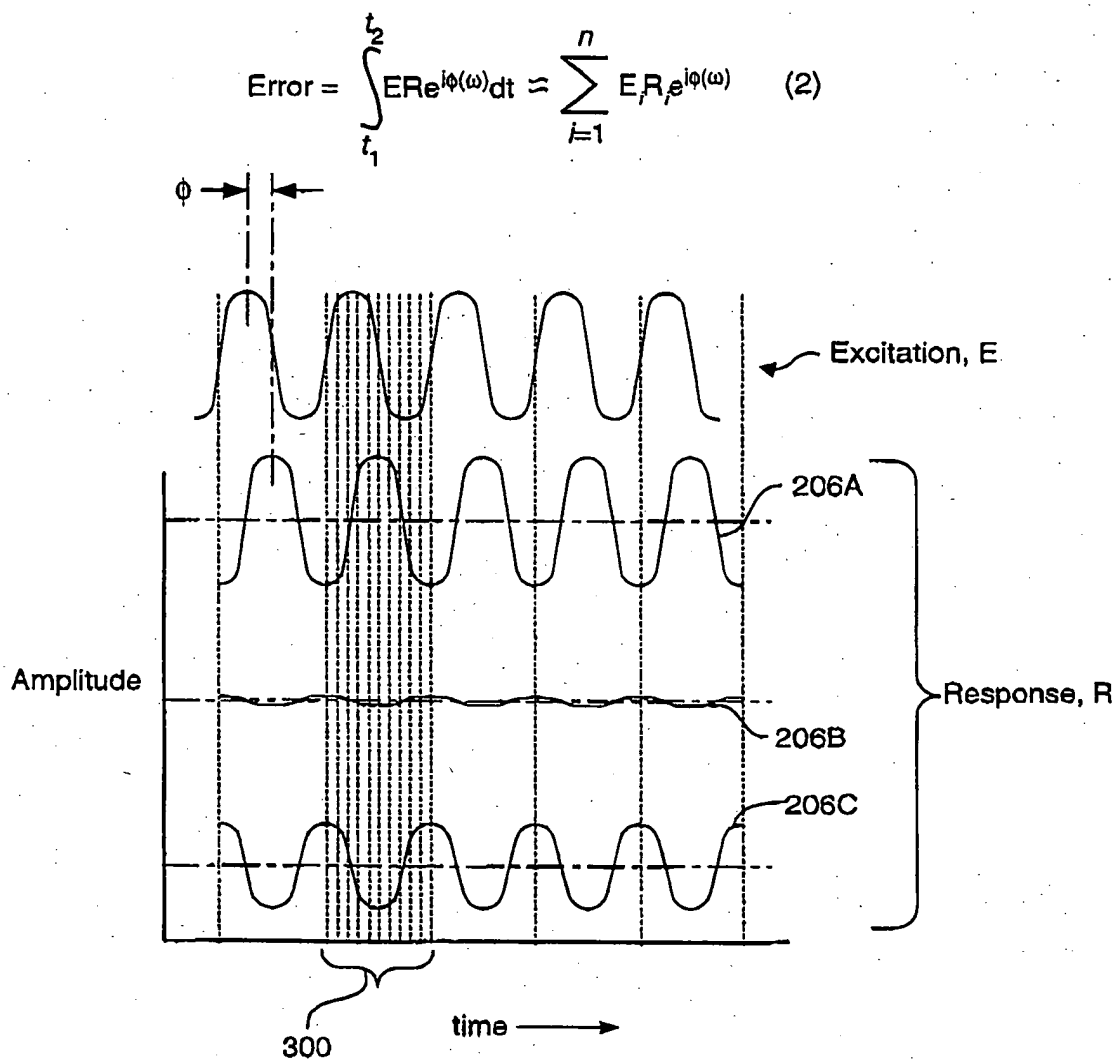


**Fig. 1**

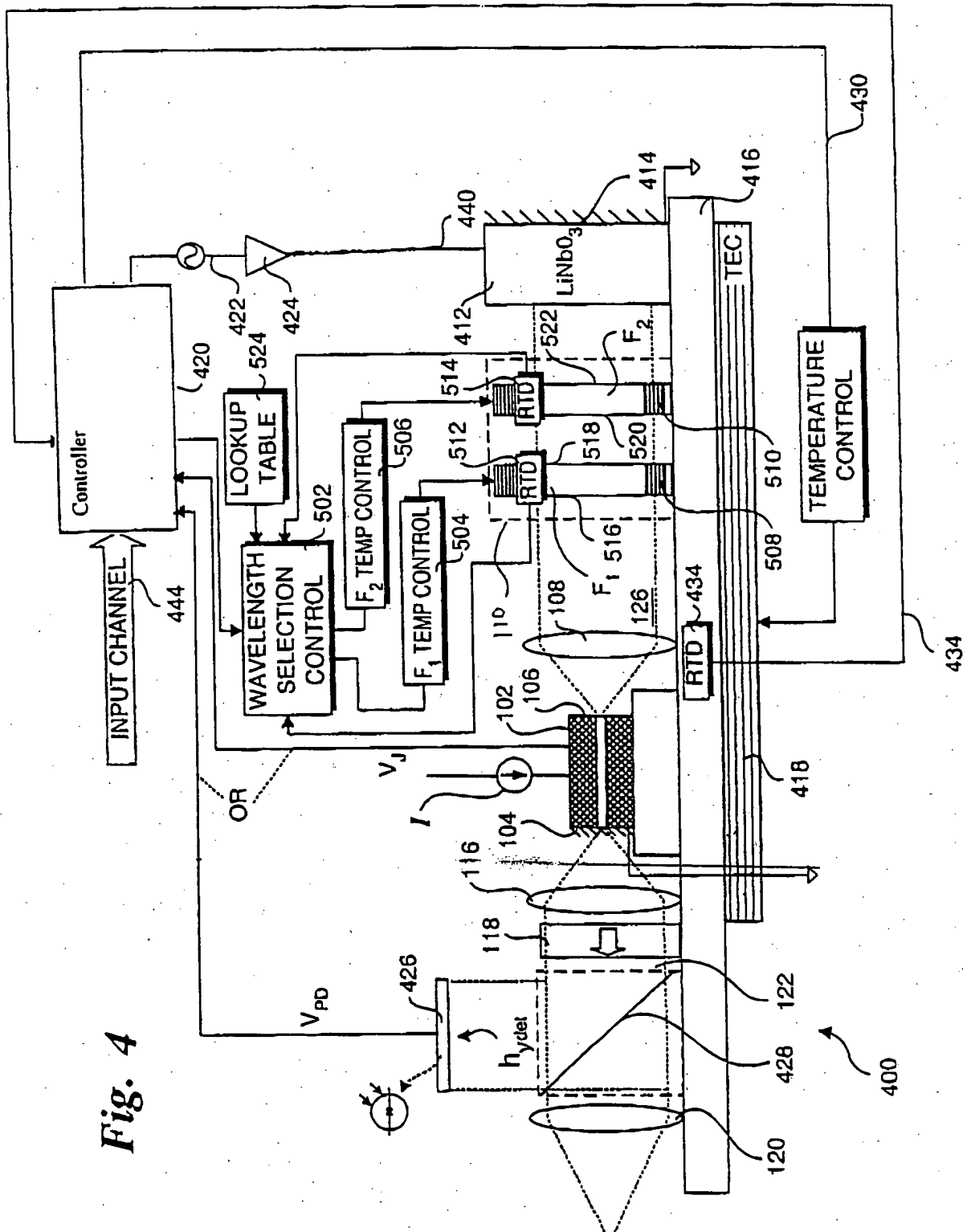


**Fig. 2**



**Fig. 3**

Fig. 4



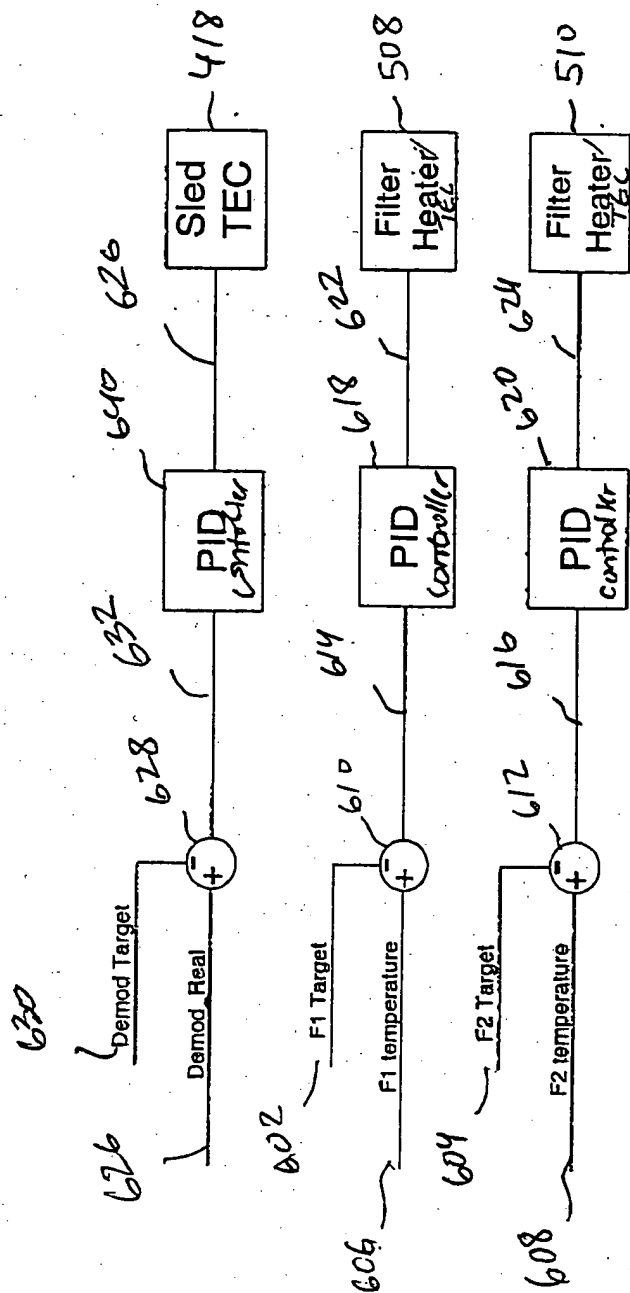


FIG. 5

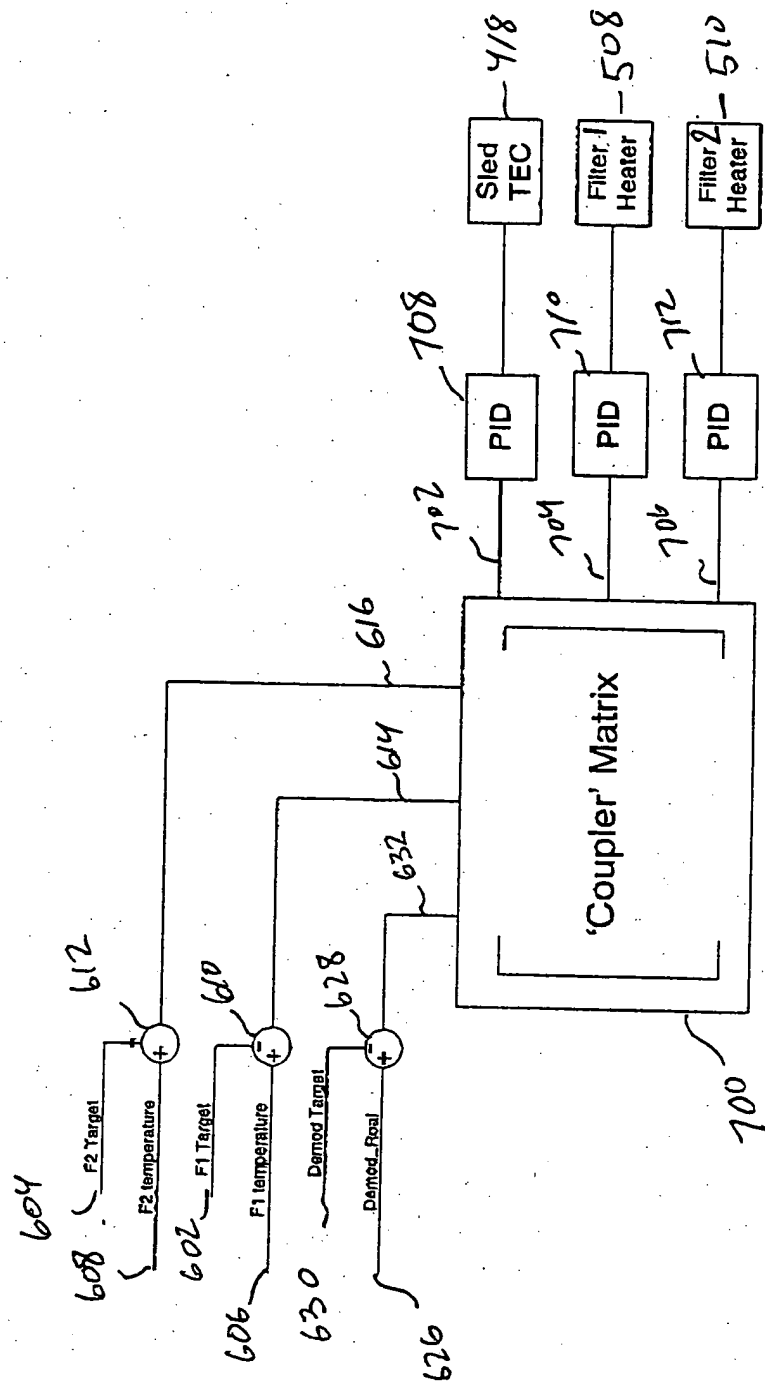


FIG. 6

$$\begin{array}{l}
 702 \\
 704 \\
 706
 \end{array}
 \begin{bmatrix}
 \text{Sled\_PID\_input} \\
 \text{F1\_PID\_input} \\
 \text{F2\_PID\_input}
 \end{bmatrix}
 \stackrel{900}{=}
 \begin{bmatrix}
 1 & 0 & 0 \\
 0 & 1 & 0 \\
 0 & 0 & 1
 \end{bmatrix}
 \stackrel{700_1}{*}
 \begin{bmatrix}
 \text{Demod\_real} \\
 \text{Filt1\_error} \\
 \text{Filt2\_error}
 \end{bmatrix}
 \stackrel{800}{\begin{array}{l}
 632 \\
 614 \\
 616
 \end{array}}$$

FIG. 7A

$$\begin{array}{l}
 702 \\
 704 \\
 706
 \end{array}
 \begin{bmatrix}
 \text{Sled\_PID\_input} \\
 \text{F1\_PID\_input} \\
 \text{F2\_PID\_input}
 \end{bmatrix}
 \stackrel{900}{=}
 \begin{bmatrix}
 0 & 0 & 0 \\
 K1 & 1 & -1 \\
 K1 & -1 & 1
 \end{bmatrix}
 \stackrel{700_2}{*}
 \begin{bmatrix}
 \text{Demod\_real} \\
 \text{Filt1\_error} \\
 \text{Filt2\_error}
 \end{bmatrix}
 \stackrel{800}{\begin{array}{l}
 632 \\
 614 \\
 616
 \end{array}}$$

FIG. 7B

$$\begin{array}{l}
 702 \\
 704 \\
 706
 \end{array}
 \begin{bmatrix}
 \text{Sled\_PID\_input} \\
 \text{F1\_PID\_input} \\
 \text{F2\_PID\_input}
 \end{bmatrix}
 \stackrel{900}{=}
 \begin{bmatrix}
 0 & K2 & K2 \\
 K1 & 1 & -1 \\
 K1 & -1 & 1
 \end{bmatrix}
 \stackrel{700_3}{*}
 \begin{bmatrix}
 \text{Demod\_real} \\
 \text{Filt1\_error} \\
 \text{Filt2\_error}
 \end{bmatrix}
 \stackrel{800}{\begin{array}{l}
 632 \\
 614 \\
 616
 \end{array}}$$

FIG. 7C